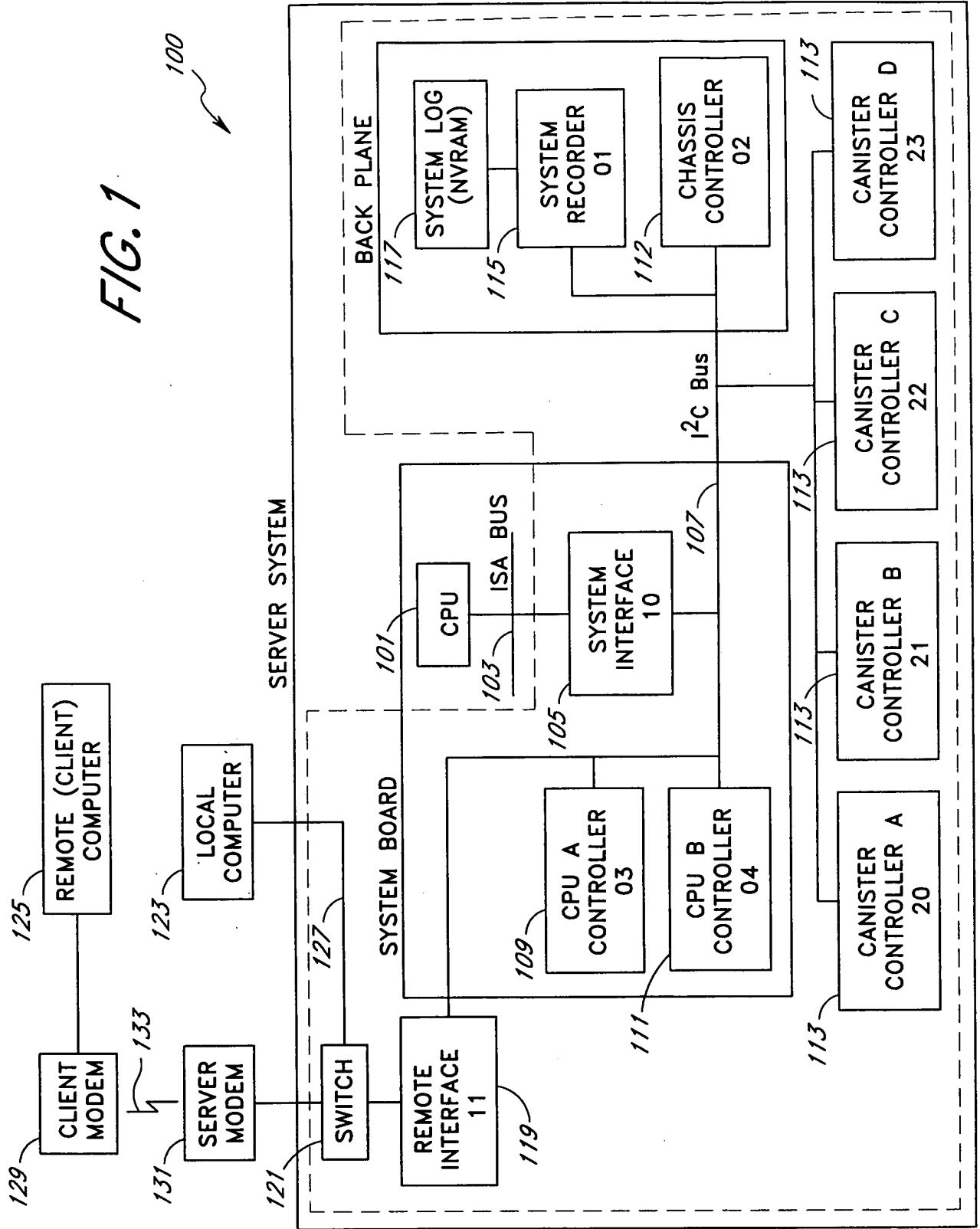


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714 48

FIG. 1



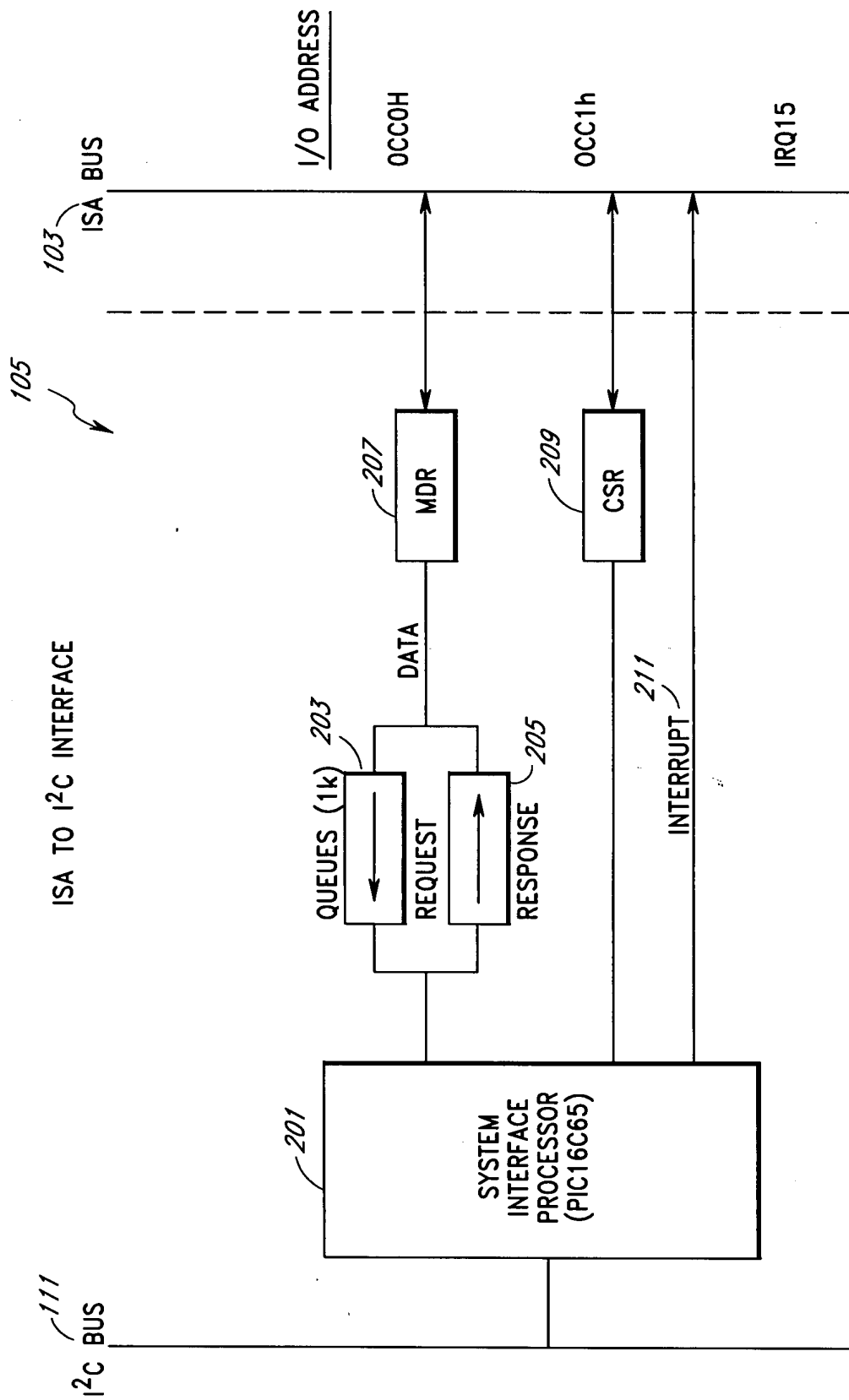


FIG.2

READ REQUEST FORMAT

Offset

Byte 0	Slave Addr (7 Bytes)	0 LSBit
Byte 1	MSBit (1)	Type
Byte 2	Command ID (LSB)	
Byte 3	Command ID (MSB)	
Byte 4	Read Request Length (N)	
Byte 5	Check Sum	

FIG.3A

WRITE REQUEST FORMAT

Offset

Byte 0	Slave Addr (7 Bytes)	0 LSBit
Byte 1	MSBit (0)	Type
Byte 2	Command ID (LSB)	
Byte 3	Command ID (MSB)	
Byte 4	Write Request Length (N)	
Byte 5	Data Byte 1	
⋮	⋮	
Byte N+4	Data Byte N	
Byte N+5	Check Sum	

FIG.3B

READ RESPONSE FORMAT

Offset

Byte 0	Slave Addr (7 Bytes)	1 LSBit
Byte 1	Read Request Length (N)	
Byte 2	Data Byte 1	
⋮	⋮	
Byte N+1	Data Byte N	
Byte N+2	Status	
Byte N+3	Check Sum	
Byte N+4	Inverted Slave Addr	

FIG.3C

WRITE REQUEST FORMAT

Offset

Byte 0	Slave Addr (7 Bytes)	1 LSBit
Byte 1	Write Response Length (N)	
Byte 2	Status	
Byte 3	Check Sum	
Byte 4	Inverted Slave Addr	

FIG.3D

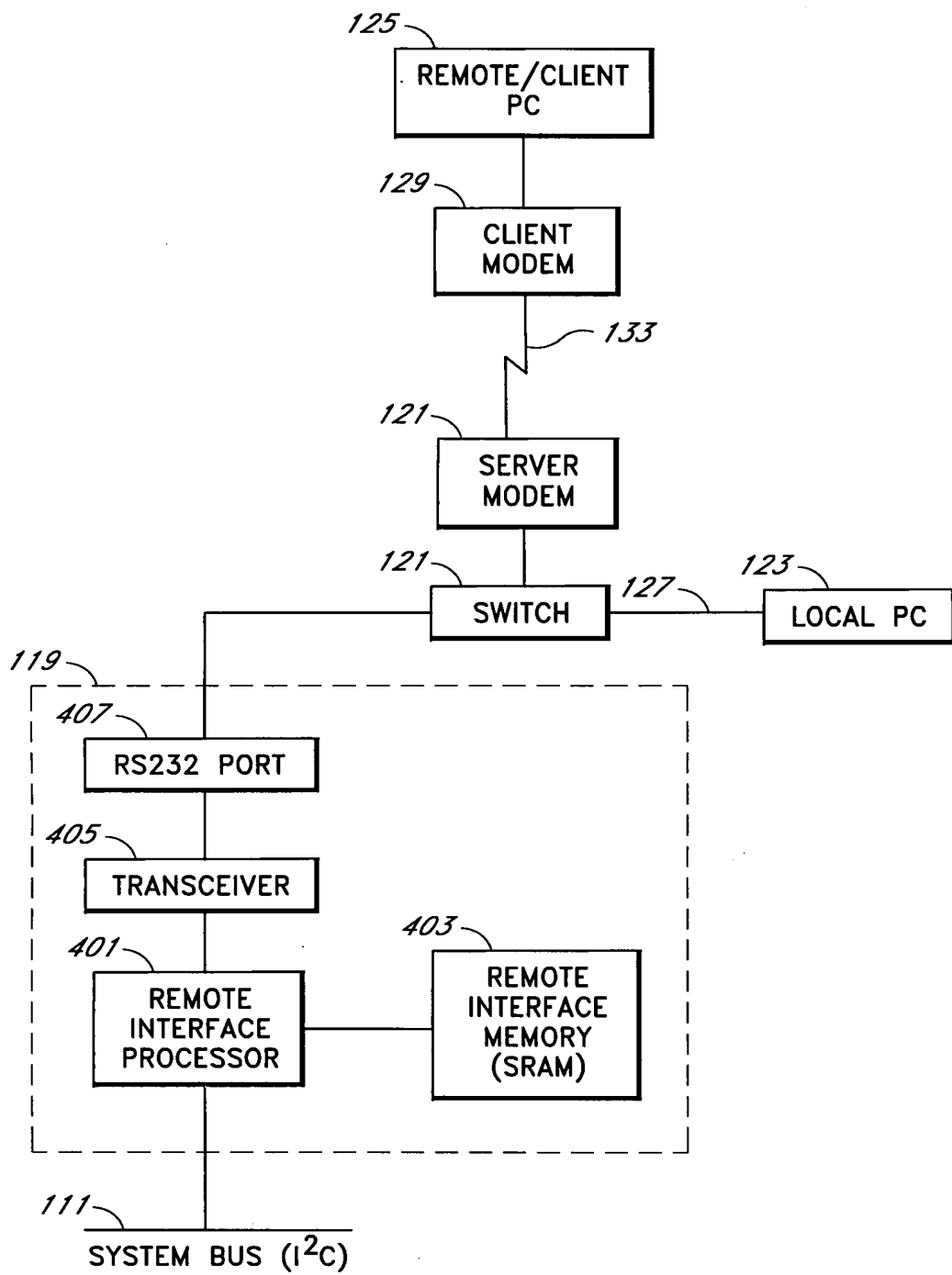


FIG. 4

REMOTE INTERFACE SERIAL PROTOCOL MESSAGE FORMATS

Request:

SOM	Seq. #	TYPE	Data	...	Check	EOM
-----	--------	------	------	-----	-------	-----

FIG. 5A

Response

SOM	Seq. #	STATUS	Data	...	Check	EOM
-----	--------	--------	------	-----	-------	-----

FIG. 5B

Event Interrupt

INT

FIG. 5C

SYSTEM BUS
(I²C)

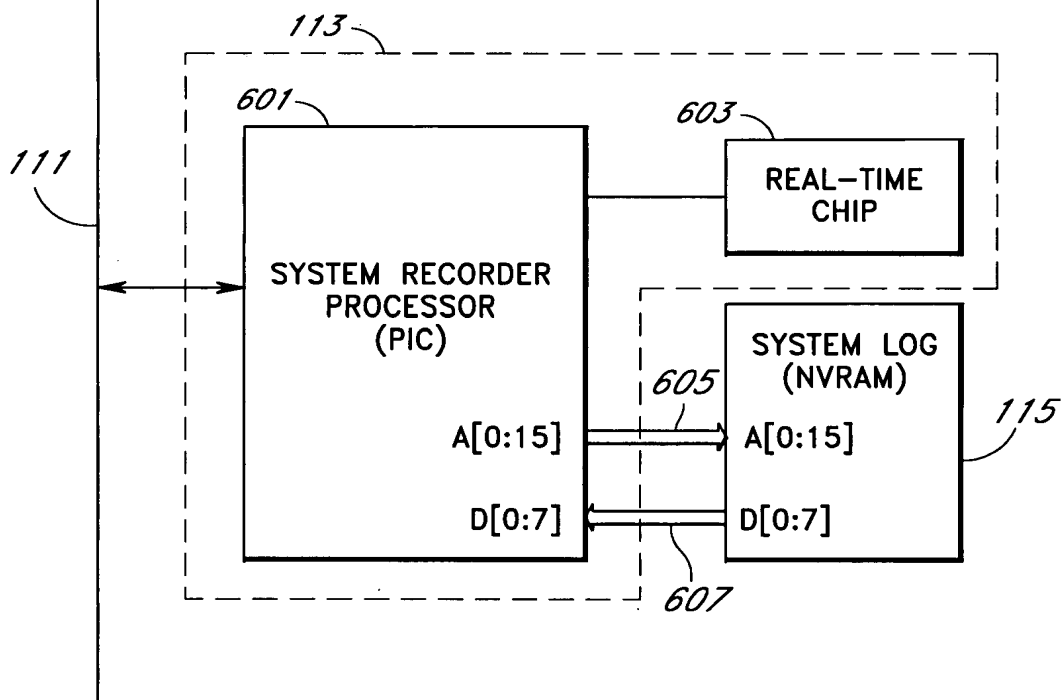


FIG. 6

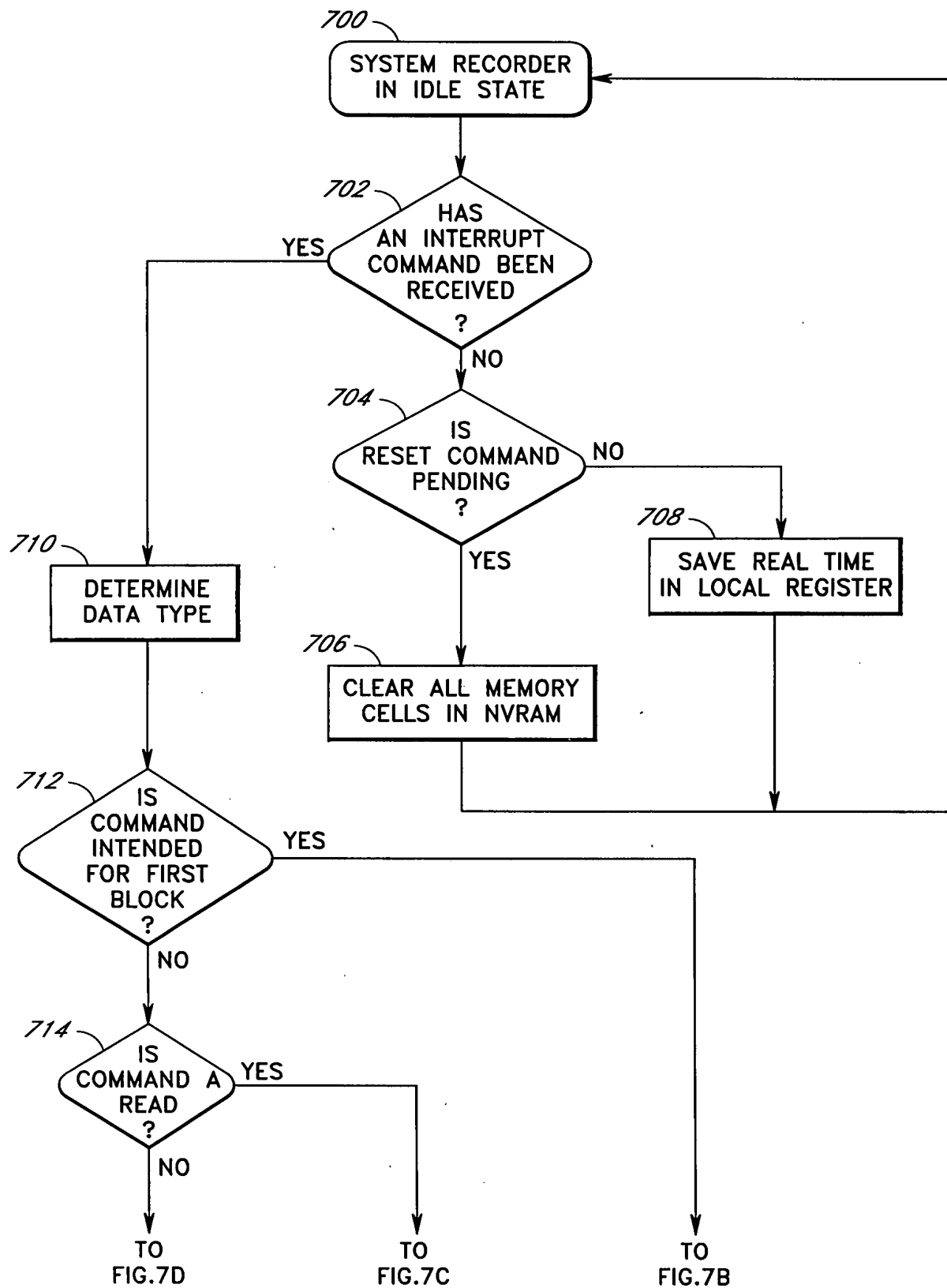


FIG. 7A

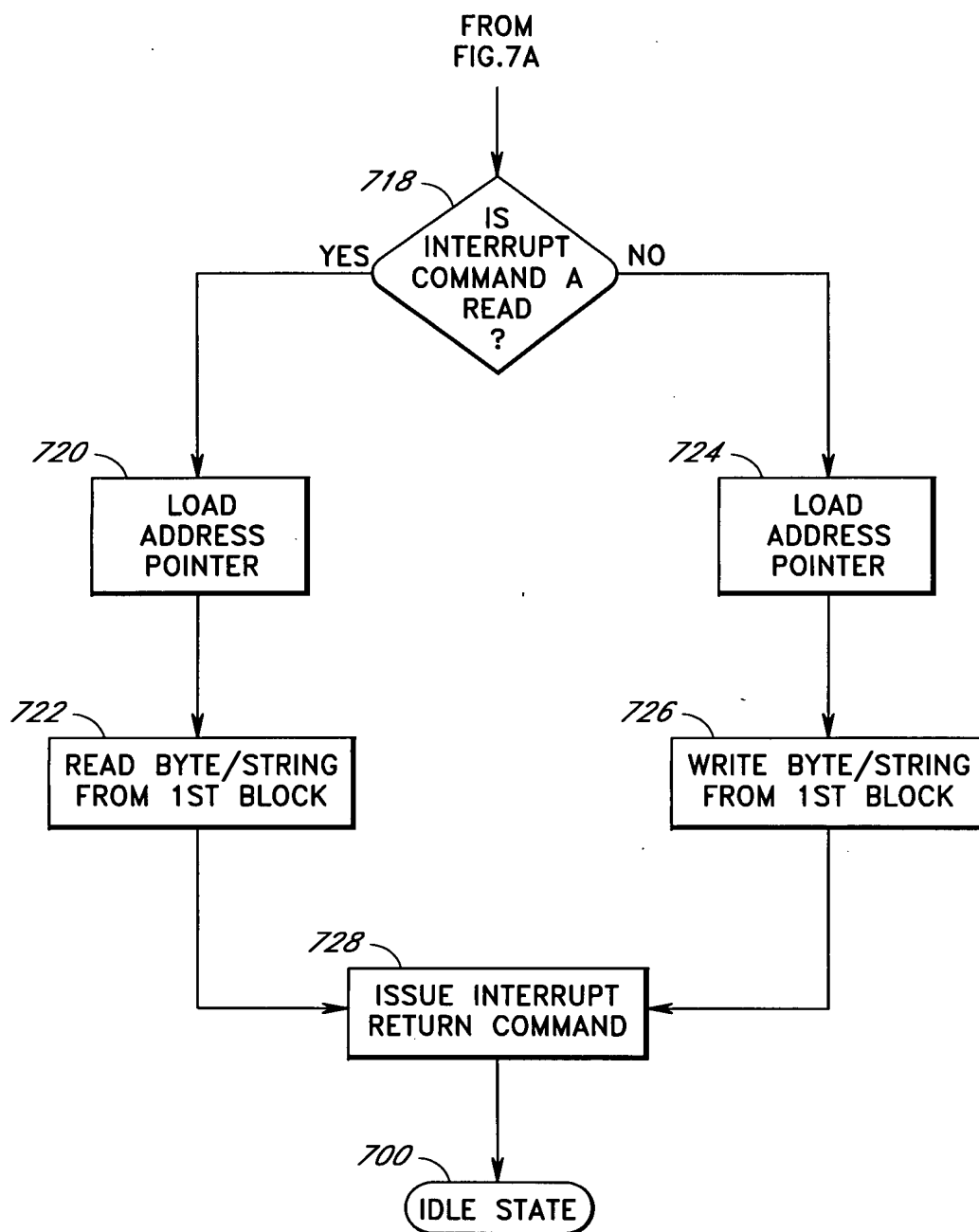


FIG.7B

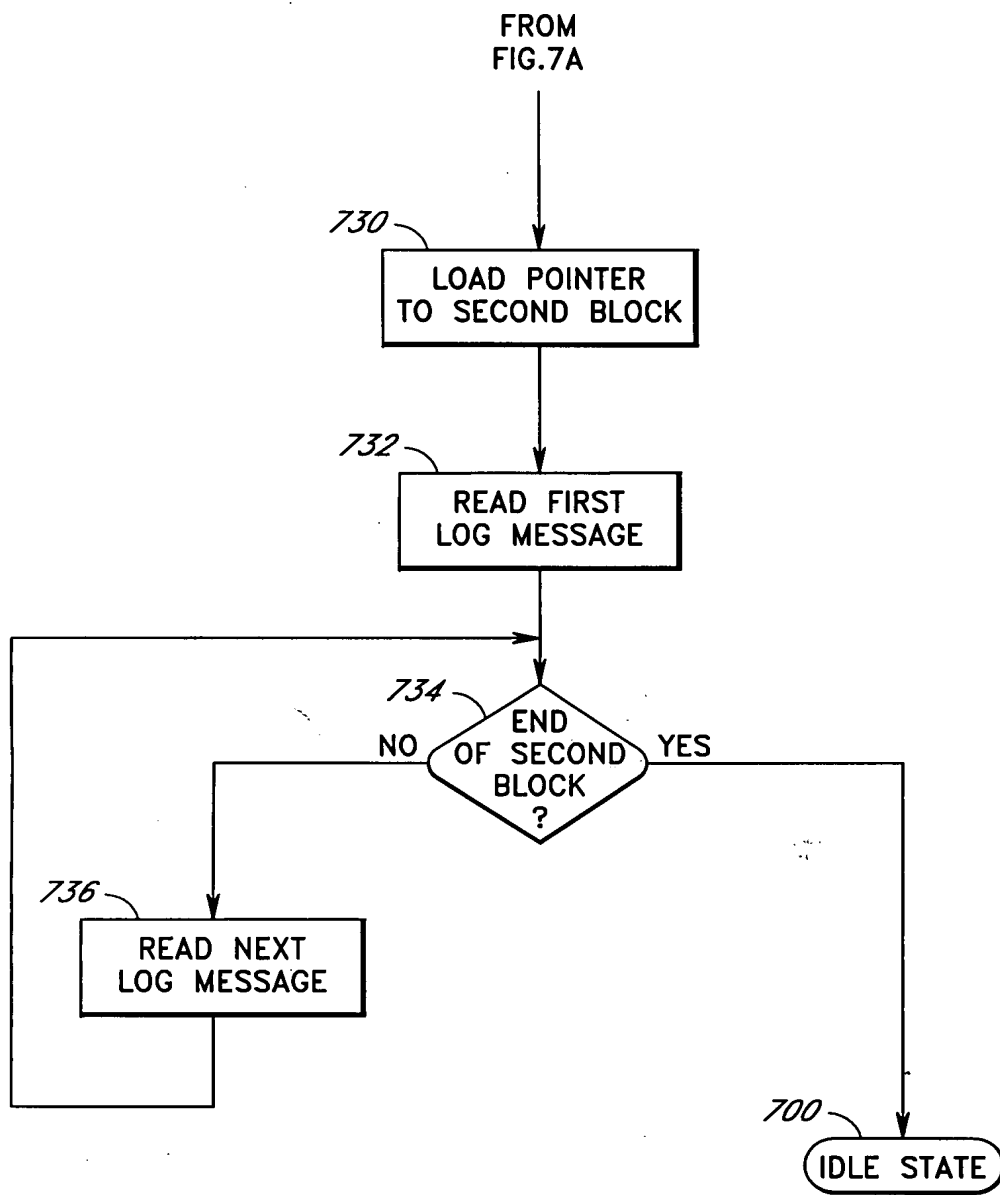


FIG. 7C

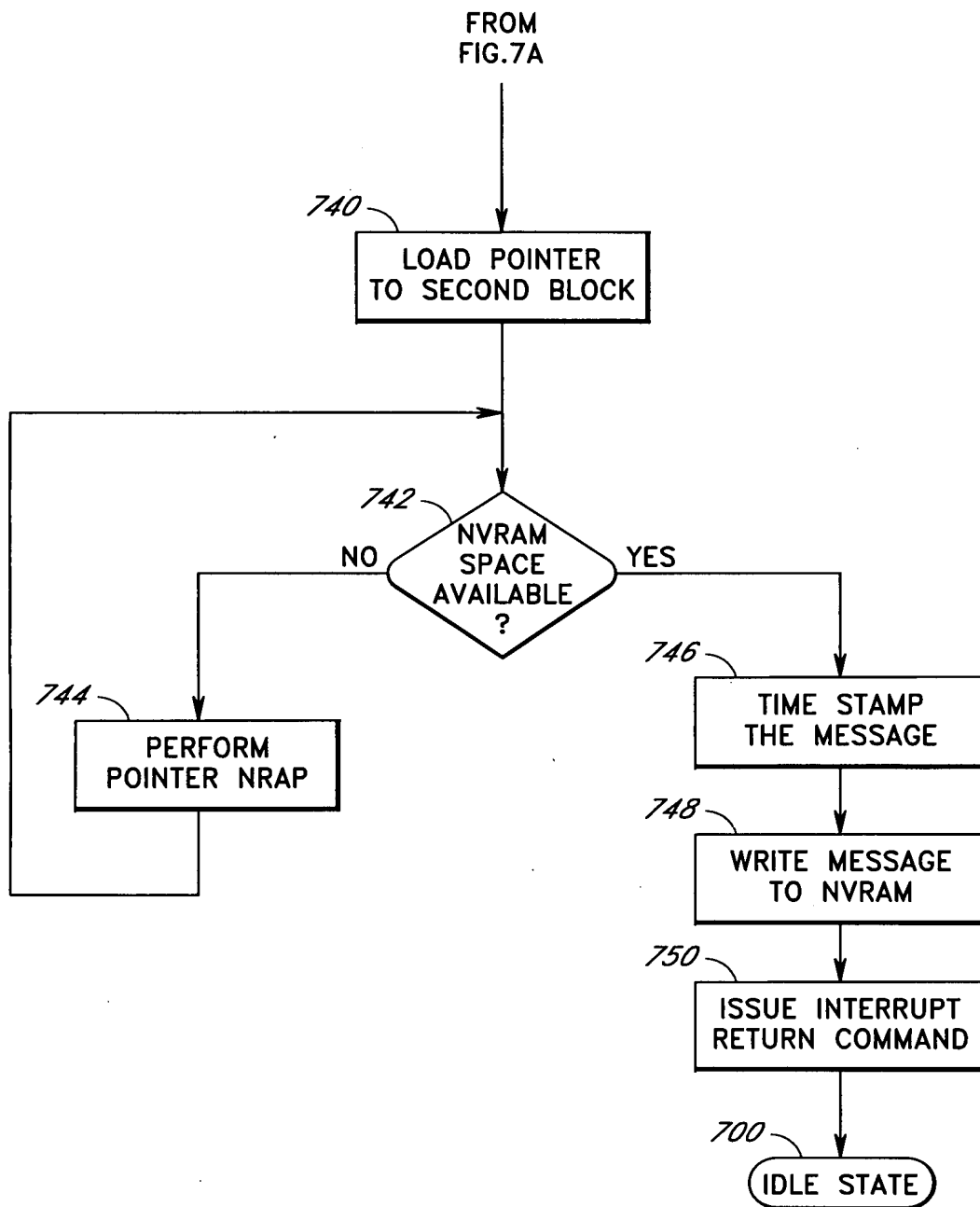


FIG.7D

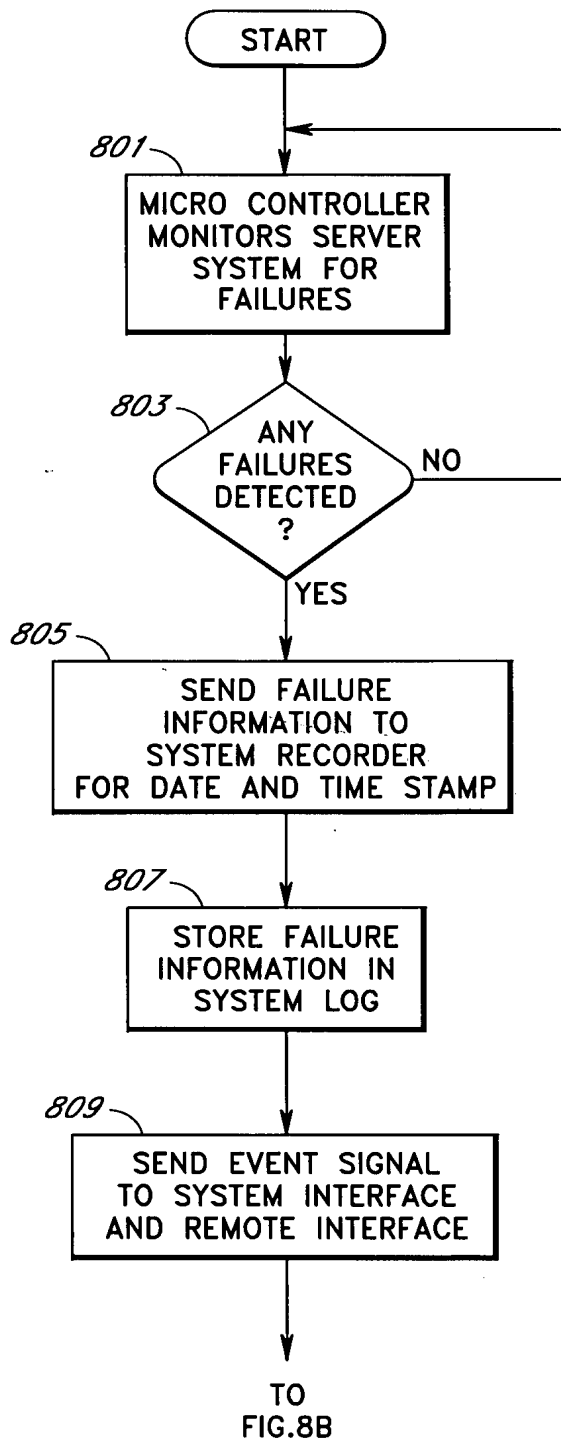


FIG. 8A

FROM
FIG.8A

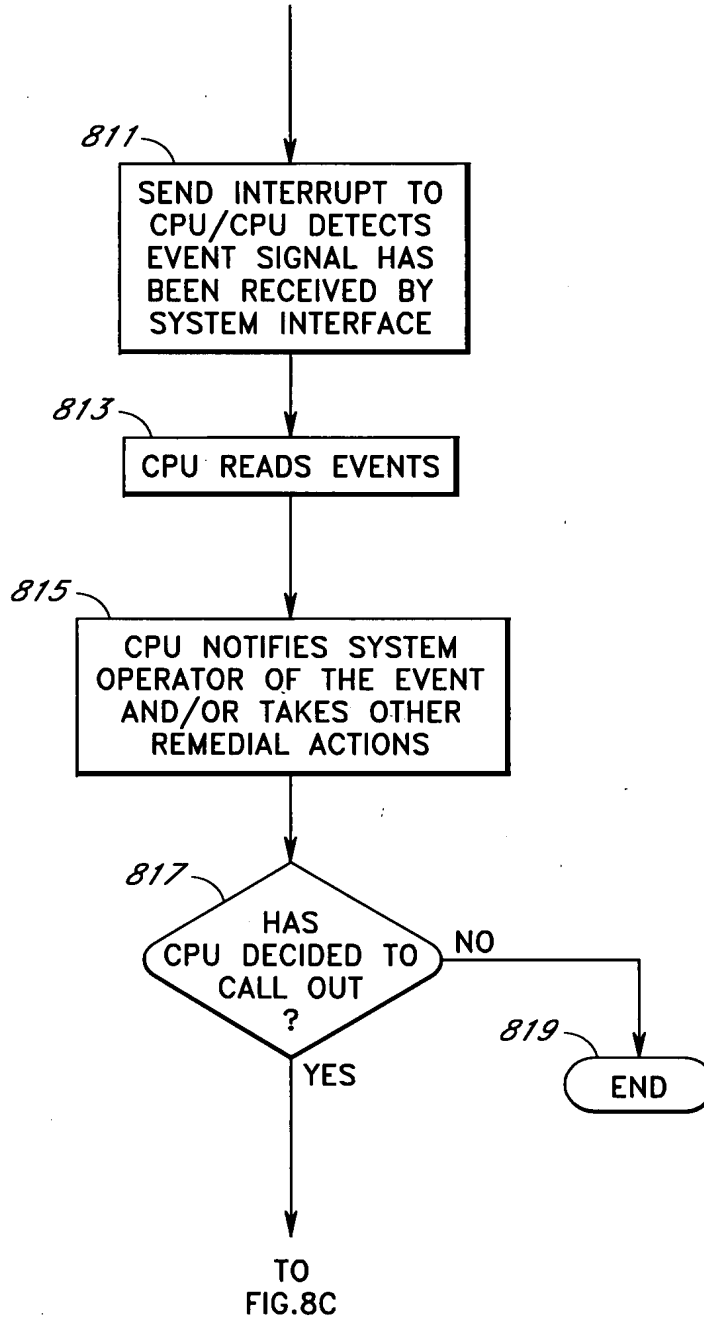


FIG.8B

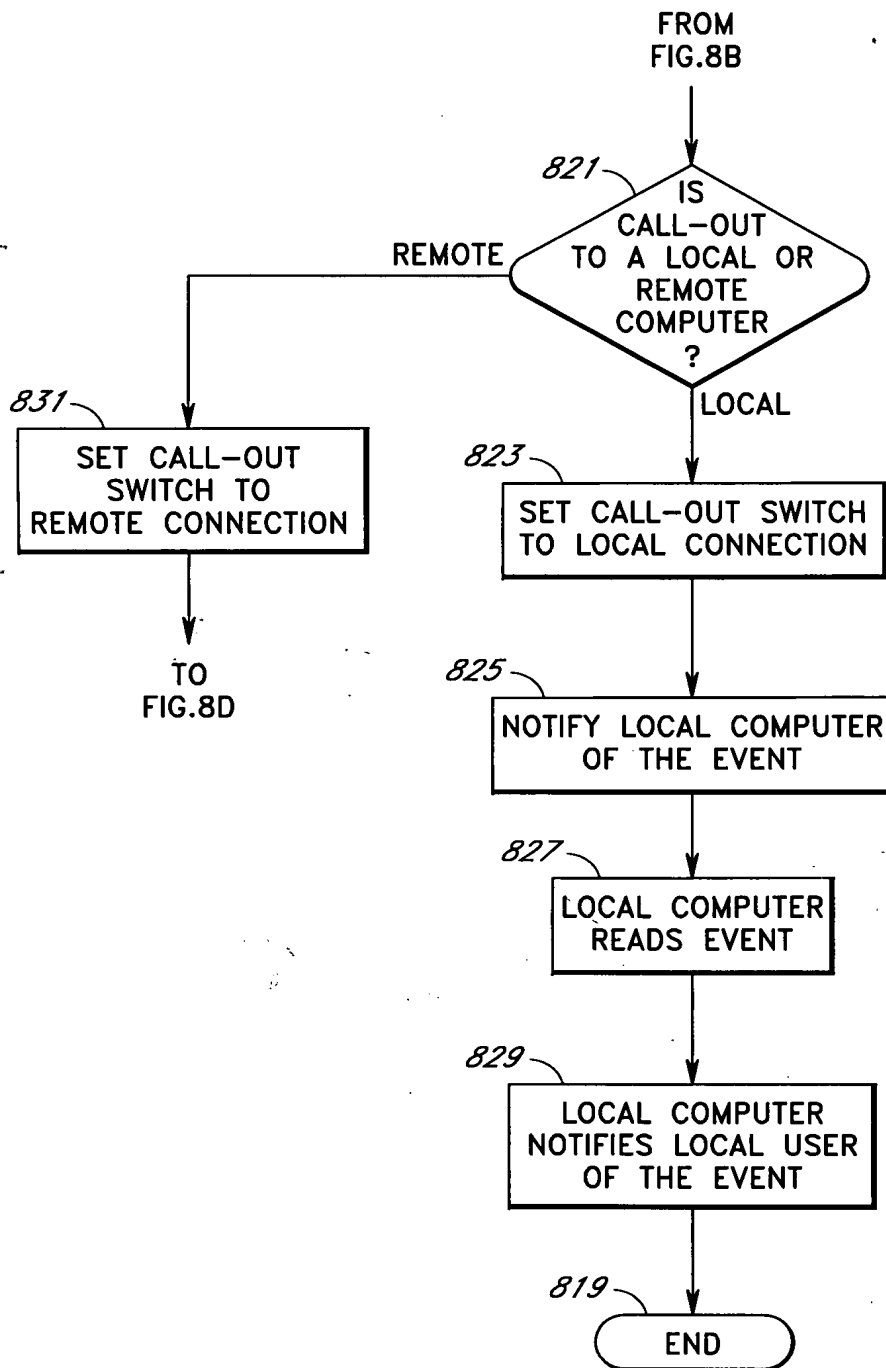


FIG.8C

$\frac{d}{dt} \left(\frac{\partial L}{\partial \dot{x}} \right) = \frac{\partial L}{\partial x}$

